



#161B ADE  
1-14-04 JC

Docket No.: M4065.0767/P767  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Barmak Mansoorian

Application No.: 09/359,056

Filed: July 21, 1999

Art Unit: 2615

Examiner: N. Tran

For: HIGH RESOLUTION CMOS CIRCUIT  
USING A MATCHED IMPEDANCE  
OUTPUT TRANSMISSION LINE

*not to Enter per REC 1-2-04*  
*nt*  
*1/15/04*

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**

JAN 09 2004

Technology Center 2600

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated October 7, 2003 (Paper No. 14), please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.